

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Raminderpal Singh, et al **Examiner:** Unassigned
Serial No.: Unassigned **Group Art Unit:** Unassigned
Filed: Herewith **Docket:** END920030032US1 (16704)
For: A VERTICALLY-STACKED CO-PLANAR
TRANSMISSION LINE STRUCTURE
FOR IC DESIGN **Dated:** August 21, 2003

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

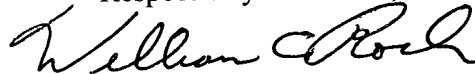
Sir:

In accordance with 37 C.F.R. §§ 1.97 and 1.98, it is requested that the five (5) references, which are listed on the attached Form PTO-1449, be made of record in the above-identified case.

Applicants are submitting copies of the non U.S. references cited in the attached form PTO-1449.

Inasmuch as this Information Disclosure Statement is being submitted in accordance with the schedule set out in 37 C.F.R. § 1.97(b), no statement or fee is required.

Respectfully submitted,



William C. Roch
Registration No.: 24,972

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INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)	Docket Number (Optional) END920030032US1 (16704)	Application Number
	Applicant(s) Raminderpal Singh, et al.	
	Filing Date Herewith	Group Art Unit

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		5,922,650	7/13/99	Ye			
		6,205,013B1	3/20/01	Gong et al.			
		2001/0040490A1	11/15/01	Tanaka			
		2002/0102835A1	8/1/02	Stucchi et al.			

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		Silicon RF Technology-The Two Generic Approaches by Joachim N. Burghartz

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.